𝓣𝓕𝓦

PATENT

Case Docket No. ZLINK.026A
Date: June 24, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	LeReverend, Remi
Appl. No.	:	10/826,891
Filed	:	April 16, 2004
For	:	TRANSISTOR WELL BIAS SCHEME
Examiner	:	Unknown
Group Art Unit:		Unknown

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

June 24, 2004
(Date)

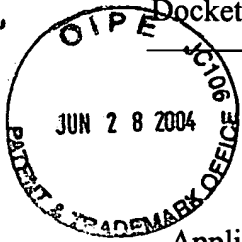Tiffany Miller, Reg. No. 52,032

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

(X)   An Information Disclosure Statement.

(X)   A PTO Form 1449 listing eight (8) references and enclosing six (6) references.

(X)   The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.

(X)   Return prepaid postcard.

Tiffany Miller
Registration No. 52,032
Agent of Record
Customer No. 20,995
(619) 235-8550

S:\DOCS\TCM\TCM-3260.DOC 061504

JUN 2 8 2004

INFORMATION DISCLOSURE STATEMENT

Applicant	:	LeReverend, Remi
App. No.	:	10/826,891
Filed	:	April 16, 2004
For	:	TRANSISTOR WELL BIAS SCHEME
Examiner	:	Unknown
Group Art Unit	:	Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 8 references. Copies of disclosed U.S. patents and/or publications are not included pursuant to PTO waiver of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for applications filed after June 30, 2003. Copies of other references, if listed, are enclosed.

This Information Disclosure Statement is being filed with an RCE or within three months of the filing date of this application and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).
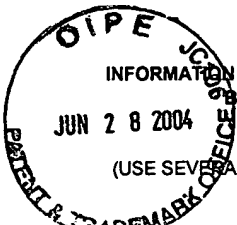
Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: June 24, 2004

By: Tiffany Miller

Tiffany Miller
Registration No. 52,032
Agent of Record
Customer No. 20,995
(619) 235-8550

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ZLINK.026A	APPLICATION NO. 10/826,891
INFORMATION DISCLOSURE STATEMENT BY APPLICANT JUN 2 8 2004 (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT LeReverend, Remi	
	FILING DATE April 16, 2004	GROUP Unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1.	6,097,242	8/1/00	Forbes et al.			
	2.	U.S. Patent Publication No.: 2002/0093382 A1	July 18, 2002	Gupta			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	3.	Eric A. Vittoz, *Micropower Techniques*, Design of VLSI Circuits for Telecommunications and Signal Processing, pp. 53-97 (J.E. Franca and Y.P. Tsividis eds., Prentice Hall, 1994)
	4.	Namkyu Park, et al., *Body Bias Dependence of 1 / f Noise in NMOS Transistors From Deep-Subthreshold to Strong Inversion*, IEEE Transactions on Electron Devices, Vol. 48, No. 5, pp. 999-1001 (May 2001)
	5.	Clement Wann, et al., *CMOS With Active Well Bias for Low-Power and RF/Analog Applications*, IEEE - 2000 Symposium on VLSI Technology Digest of Technical Papers, pp. 158-159
	6.	Ming-Jer Chen, et al., *Dependence of Current Match on Back-Gate Bias in Weakly Inverted MOS Transistors and its Modeling*, IEEE Journal of Solid-State Circuits, Volume 31, No. 2, pp. 259-262 (February 1996)
	7.	Shih-Fen Huang, et al., *Scalability and Biasing Strategy for CMOS with Active Well Bias*, 2001 Symposium on VLSI Technology Digest of Technical Papers, pp. 107-108
	8.	Rajesh H. Zele, *Low-Power CMOS Continuous-Time-Filters*, IEEE Journal on Solid-State Circuits, Volume 31, No. 2, pp. 157-168 (February 1996)

S:\DOCS\TCM\TCM-3261.DOC 061504

EXAMINER	DATE CONSIDERED

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.